# Performance of the Infinet Wireless devices



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#### Introduction

This document is intended to demonstrate Infinet Wireless devices potential performance. The data used in the document is obtained through laboratory testing. The parameters of communication channels in real conditions may differ from the presented, however, under conditions of minimal external influence, the obtained values are achievable in practice.

The link quality parameters depend on two factors: external conditions and wireless devices configuration. An environmental factors are a set of characteristics related to the formation, distribution and reception of a radio signal, the mounting quality, etc. The complex influence of these factors is unique and depends on the specific case, therefore the task of reproducing the possible situations diversity is time-consuming. The modulation code scheme decreasing is a consequence of signal-to-noise ratio (SNR) decreasing. Therefore, the possible influence of external factors during laboratory testing was taken into account in the initial data. The wireless networking fundamentals are detaily described in the corresponding online course available on the IW Academy portal.

The parameter configuration influence will be described within this document. The article contains several Infinet Wireless hardware platforms, characteristic set of parameters that affect the performance will be specified for each.

# Factors which determines channel characteristic

# The modulation code scheme

The selected modulation code scheme for data transmission determines the communication channel throughput. Let's look at the example (see Figure 1) to confirm the statment.

Let's look at the example, we will transmitt 6 bit of information - "100111". In the first case, for the information sequence transmission, we will use the QPSK 3/4 modulation code scheme, in the second case - QAM16 1/2. The parity check code is used as the error-control code.

At the Figure 1 signal constellations for modulation code schemes QPSK 3/4 and QAM16 1/2 are shown. Each point corresponds to a certain amplitude value (A) and phase (p) of the signal, and a set of bits is assigned to each of these signals. By forming a signal with given amplitude and phase values, the corresponding data set is transmitted, and the set of all signals (constellation points) is called allowed states. The greater the number of allowed states, the more bits can be transmitted by a signal element with given amplitude and phase characteristics.

When using QPSK 3/4 modulation, each three bits information sequence will be complemented by one bit of parity check, those instead of the original six bits sequence, eight bits will be transmitted - "10011111". The allowed signal states number for QPSK modulation is four (2²), therefore, one signal per unit of time (signal element) can transmit two bits of an error-control sequence. Thus, to transmit eight bits of the received sequence, four signal elements (8/2 = 4 signal elements) are required.

For QAM16 1/2 modulation, each information bit is complemented by one parity check bit — the original six-bit sequence is converted to a 12-bit sequence "110000" 111111". The allowed signal states number for QAM16 1/2 modulation is sixteen (2<sup>4</sup>), therefore, one signal per unit of time (signal element) can transmit four bits of an error-control sequence. Thus, to transmit twelve bits of the received sequence, three signal elements (12/4=3 signal elements) are required.

Thus, six bits of the original information sequence using QPSK 3/4 were transmitted in four time slots, and using QAM16 1/2 - in three, this demonstrates the QAM16 1/2 modulation code scheme advantage in channel and information rate. The high modulation code schemes using can improve the communication channel performance by increasing the throughput, however, as it is shown in the online course, the high modulation code schemes using is possible only for radio channels with a large link margin.

In the InfiLINK 2x2, InfiMAN 2x2, InfiLINK Evolution and InfiMAN Evolution families devices, the modulation code scheme type is shown as a bitrate, in the InfiLINK XG and InfiLINK XG 1000 families devices, MCS itself is displayed. In order to eliminate confusions, in the document below the MCS (modulation code scheme) index will be used. The correspondence between modulation and index is shown in the table below.

Table of the correspondence between modulation code scheme and bitrate in InfiLINK 2x2, InfiMAN 2x2 devices

| Channel width, MHz           |                 | 5             | 10    | 20     | 40     |
|------------------------------|-----------------|---------------|-------|--------|--------|
| Modulation code scheme index | Modulation type | Bitrate, Kbps |       |        |        |
| MCS15                        | QAM64 5/6       | 32000         | 65000 | 130000 | 300000 |
| MCS14                        | QAM64 3/4       | 29000         | 58000 | 117000 | 270000 |
| MCS13                        | QAM64 2/3       | 26000         | 52000 | 104000 | 240000 |
| MCS12                        | QAM16 3/4       | 19000         | 39000 | 78000  | 180000 |
| MCS11                        | QAM16 1/2       | 13000         | 26000 | 52000  | 120000 |
| MCS10                        | QPSK 3/4        | 9000          | 19000 | 39000  | 90000  |
| MCS9                         | QPSK 1/2        | 6000          | 13000 | 26000  | 60000  |
| MCS8                         | BPSK 1/2        | 3000          | 6000  | 13000  | 30000  |

Table of the correspondence between modulation code scheme and bitrate in InfiLINK Evolution, InfiMAN Evolution

| Channel width, MHz           |                 | 20     | 40            | 80     |
|------------------------------|-----------------|--------|---------------|--------|
| Modulation code scheme index | Modulation type |        | Bitrate, Kbps |        |
| MCS 9                        | QAM256 5/6      | -      | 400000        | 866700 |
| MCS 8                        | QAM256 3/4      | 173300 | 360000        | 780000 |
| MCS 7                        | QAM64 5/6       | 144400 | 300000        | 650000 |
| MCS 6                        | QAM64 3/4       | 130300 | 270000        | 585000 |
| MCS 5                        | QAM64 2/3       | 115600 | 240000        | 520000 |
| MCS 4                        | QAM16 3/4       | 86700  | 180000        | 390000 |
| MCS 3                        | QAM16 1/2       | 57800  | 120000        | 260000 |
| MCS 2                        | QPSK 3/4        | 43300  | 90000         | 195000 |
| MCS 1                        | QPSK 1/2        | 28900  | 60000         | 130000 |
| MCS 0                        | BPSK 1/2        | 14400  | 30000         | 65000  |

Table of the correspondence between MCS index and modulation type in InfiLINK XG/InfiLINK XG 1000 family devices

| Modulation code scheme index | Modulation type |  |
|------------------------------|-----------------|--|
| MCS10                        | QAM1024 8/10    |  |
| MCS9                         | QAM256 30/32    |  |

| MCS8 | QAM256 7/8 |
|------|------------|
| MCS7 | QAM256 6/8 |
| MCS6 | QAM64 5/6  |
| MCS5 | QAM64 4/6  |
| MCS4 | QAM16 3/4  |
| MCS3 | QAM16 1/2  |
| MCS2 | QPSK 3/4   |
| MCS1 | QPSK 1/2   |
| MCS0 | QPSK 1/4   |

Table of the correspondence between MCS index and modulation type in Quanta 5, Quanta 6 families devices

| Modulation code scheme index | Modulation type |
|------------------------------|-----------------|
| MCS 13                       | QAM256 7/8      |
| MCS 12                       | QAM256 13/16    |
| MCS 11                       | QAM256 3/4      |
| MCS 10                       | QAM64 5/6       |
| MCS 9                        | QAM64 3/4       |
| MCS 8                        | QAM64 2/3       |
| MCS 7                        | QAM16 3/4       |
| MCS 6                        | QAM16 5/8       |
| MCS 5                        | QAM16 1/2       |
| MCS 4                        | QPSK 3/4        |
| MCS 3                        | QPSK 5/8        |
| MCS 2                        | QPSK 1/2        |
| MCS 1                        | QPSK 1/3        |
| MCS 0                        | QPSK 1/4        |

Table of the correspondence between MCS index and modulation type in Quanta 70 family devices

| Modulation code scheme index | Modulation type |
|------------------------------|-----------------|
| MCS 7                        | QAM64 5/6       |
| MCS 6                        | QAM64 2/3       |
| MCS 5                        | QAM16 3/4       |
| MCS 4                        | QAM16 1/2       |
| MCS 3                        | QPSK 3/4        |
| MCS 2                        | QPSK 1/2        |
| MCS 1                        | BPSK 1/2        |
| MCS 0                        | BPSK 1/4        |

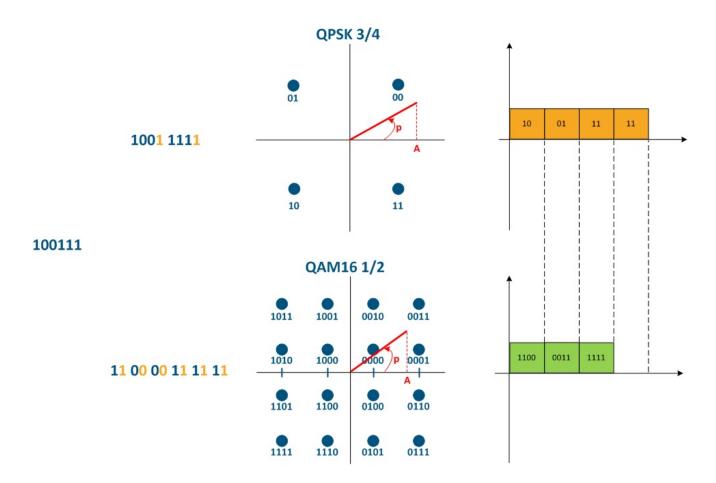


Figure 1 - The impact of the modulation code scheme on the communication channel throughput

# Frame size

In practice, at the same time, multiple frames of different length are transmitted in the network, so it is useful to evaluate the link quality parameters by generating traffic consisting of frames with a given size. Testing is performed for frames with sizes from 64 to 1518 bytes, which is regulated by the Ethernet protocol.

To explain the frame size effect on the useful throughput of the communication channel, let's look at the Ethernet frame structure (see Figure 2a). An Ethernet frame consists of two service fields with constant length: a header (14 bytes) and a checksum field (4 bytes), and a data field (46-1500 bytes), i.e. total frame size is in the range of 64-1518 bytes.

Let's look at the task in which we need to transfer 1500 bytes of information by placing the data in Ethernet frames. In order to do this we will use frames of different lengths: 1518 bytes (*Figure 2b*), 518 bytes (*Figure 2c*), 268 bytes (*Figure 2d*). Summary information about the service headers size and its share in the data stream is given in the table below. We can see that with decreasing frame size, the cost of transmitting service headers significantly increases, while the share of payload data decreases.

| Frame size | Total frames size | Headers size | Share of headers |
|------------|-------------------|--------------|------------------|
| 1518 bytes | 1518 bytes        | 18 bytes     | 1.19%            |
| 518 bytes  | 1554 bytes        | 54 bytes     | 3.47%            |
| 268 bytes  | 1608 bytes        | 108 bytes    | 6.72%            |

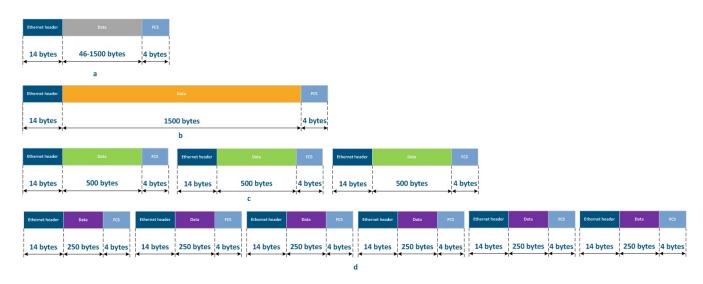


Figure 2 - Ethernet frame structure: a - general structure, b - frame with 1500 bytes data field, c - frames with 500 byte data fields, d - frames with 250 byte data fields

With decreasing frame size, the share of overhead costs for service headers (regulated by the requirements of the data transfer protocol, as shown in the table above) increases. The solution for improving the efficiency of data transfer by using maximum length frames seems obvious. However, small frames are processed faster by network equipment, it reduces the time to propagate a frame from source to destination. Distribution time is critical parameter for some types of traffic. For real-time traffic, such as voice traffic, a reduction in frame size is forced, because the key indicator of the link quality in this case is not the throughput, but the delay value. Therefore, the data should be sent as quickly as possible, in smaller portions.

In general, the delay value depends on:

- the radio signal propagation time (depends on the propagation medium);
- frame processing time (depends on the device's packet performance);
- time spent in the queue (depends on the link loading level, the size of the device memory buffers and QoS configuration).

# Frequency channel width

When a radio channel is established between two devices, F1 carrier frequency is selected for them, the spectrum of which is shown in Figure 3a. Such a signal does not carry information, since its main parameters (amplitude, frequency, phase) are known and unchanged, i.e. it's possible to predict the state of the signal in any future time period. In order for the radio signal to become an information carrier, its parameters are changed in accordance with the data flow. This process is called modulation. In the modulation process, the basic parameters of the signal are changed and its spectrum is transformed - now the signal occupies a certain frequency band  $\Delta F$  (see Figure 3b).

Note that the frequency channel width affects the available set of modulation code schemes and the link performance. Thus, the radio link capacity increasement can be accessed by expanding the occupied bandwidth.

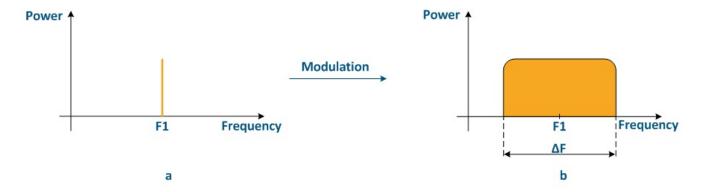


Figure 3 - Signal spectrum: a - carrier frequency, b - modulated signal

#### Radio frame period

To explain the radio frame period effect on the link quality parameters, let's look at the connection of two devices (InfiLINK XG/InfiLINK XG 1000 or R5000 with TDMA firmware). The transmitting information mechanism in the radio medium is presented below (see Figure 4). For detailed description of the data transfer mechanisms for TDMA and Polling see TDMA and Polling: Application features. A radio frame is a time period within which a subscriber terminal is serviced by a sector. During one radio frame, data is exchanged between two devices in the downstream and upstream.

Similar to Ethernet frames, a part of the radio frame is allocated for service traffic. In particular, the synchronization field and guard interval are obligatory and do not depend on radio frame period The remaining time useful data is transmitted. Thus, same as the Ethernet frames, a longer radio frame length will lead to more efficient utilization of the communication channel, but the delay value will increase (the next data packet can be transmitted only in the next transmission interval, and the longer the radio frame is, the longer the waiting time).

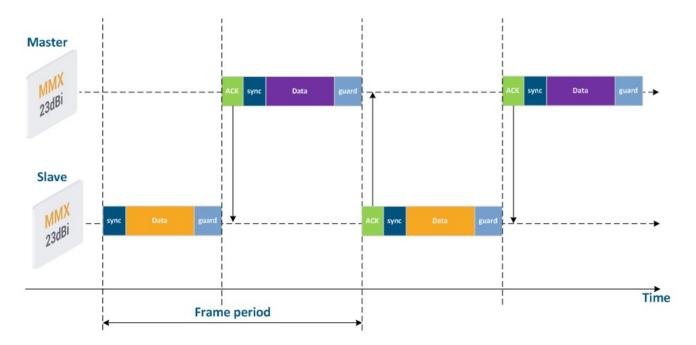


Figure 4 - The transmitting data mechanism for TDMA and TDD

### Distance range

The "Maximum distance" parameter affects the guard interval value (see Figure 4), and also the efficiency of using the link throughput. In addition, the actual farness of the subscriber terminal affects the radio signal propagation time in the media.

### Number of subscriber devices

Let's add to the picture from the "Radio frame period", section two more subscriber devices (see Figure 5).

The transmission waiting time is proportional to the number of subscriber devices, since a time interval equal to the frame period is allocated for data exchange with each station. Thus, the delay value depends on the number of active subscribers.

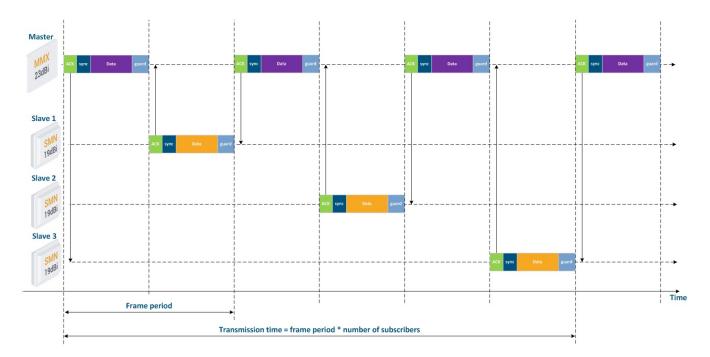


Figure 5 - The transmitting data mechanism for TDMA with three subscriber terminals

### Propriate parameters set

#### Greenfield

The Greenfield option is activated on both Master and Slave devices and is responsible for optimization of service information transmitted via the radio link. Optimization significantly reduces the amount of service information and improve performance by 10-15% due to an increase of useful data share in the radio frame.

### VBR

The VBR option (*variable bitrate*) can be configured on the slave device and allows to increase efficiency of the service data transmission. With the VBR option disabled, the Sync message service fields are transmitted using a minimum modulation code scheme. When VBR is enabled, Sync fields will be transmitted on the modulation code scheme higher then the minimal, if possible. This allows to transmit the sync sequence faster, reduces the share of service messages in the total transmission time and increases the link performance

The VBR option is available only on the InfiLINK 2x2, InfiMAN 2x2 families devices with software that supports TDMA technology and on the InfiLINK Evolution, InfiMAN Evolution families devices.